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CALL FOR PAPERS
2nd International Workshop on HyperTransport Research and Applications
(WHTRA2011)

February 9th, 2011
University of Heidelberg
in Mannheim, Germany

<http://whtra.uni-hd.de>

to be held in conjunction with the
3rd Symposium of the HyperTransport Center of Excellence

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The 2nd International Workshop for Research on HyperTransport is an international high quality forum for scientists, researchers and developers working in the area of HyperTransport or related topics. Thus, this includes not only developments and research in HyperTransport itself, but also work which is based on or enabled by HyperTransport, or which could be applied to HyperTransport. Besides regular contributions we actively encourage papers about ongoing research, including new ideas and learned lessons, ensuring up-to-date presentations during the workshop. The quality of the contributions is ensured by a peer review process. Submissions will be accepted primarily for their impact on the HyperTransport community and their innovative aspect. Accepted contributions will be made available on the website of the HTCE as well as on HeiDOK, the Open Access document server of the University of Heidelberg. The publication platform offers free access to full-text documents and adheres to the principles of OpenAccess as well as the goals of the Budapest Open Access Initiative (BOAI). The papers will be accessible through a special sub-portal and are fully citable. For more information please consult the workshop website (<http://whtra.uni-hd.de>).

Papers are solicited in fields including (but not limited to) the following:

- o High performance, "green" or embedded system architectures
- o Interconnection networks
- o Network interfaces
- o On-chip networks
- o Fine grain computational problems and applications
- o Application specific processors and accelerators
- o Reconfigurable architectures
- o High scalability systems
- o HT enabled applications
- o HT caves, tunnels, switches and bridges

This workshop takes place on a regular basis to ensure a continuous knowledge exchange and to strengthen collaborations within the community.

Background

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HyperTransport (HT) is an interconnection technology which is typically used as system interconnect in modern computer systems, connecting the CPUs among each other and with the I/O bridges. Primarily designed as interconnect between high performance CPUs it provides an extremely low latency, high bandwidth and excellent scalability. The definition of the HTX connector allows the use of HT even for add-in cards. In opposition to other peripheral interconnect technologies like PCI-Express no protocol conversion or intermediate bridging is necessary. HT is a direct connection between device and CPU with minimal latency. Another advantage is the possibility of cache coherent devices. Because of these properties HT is of

high interest for high performance I/O like networking and storage, but also for co-processing and acceleration based on ASIC or FPGA technologies. HT's low latency allows for fine grain communication schemes and is perfectly suited for scalable systems.

Location and Event

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The Workshop for Research on HyperTransport will take place as an accompanying event to the Third Symposium of the HyperTransport™ Center of Excellence (HTCE) (<http://htce2011.uni-hd.de>) in Mannheim, Germany. Exchange between the participants of the Symposium and the Workshop is ensured by the possibility of mutual participation. Both events are hosted by the Department of Computer Engineering of the University of Heidelberg (<http://www.ziti.uni-heidelberg.de>). Please check the website of the workshop and the accompanying symposium for travel, hotel and further information.

Submission

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All papers must be submitted in English and as a PDF document, along with a short abstract. Authors of accepted papers are required to complete and return a signed copyright agreement. Submissions must be formatted according to the IEEE standard double column format with a font size 10pt.

A paper may not exceed 10 pages. Only original research results are accepted. Papers submitted to other conferences or journals will be rejected without review. On the workshop website detailed formatting instructions are provided. (<http://whtra.uni-hd.de>)

Important Dates

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Paper Submission Deadline: December 31, 2010 (no further extensions)

Author Notification: January 20, 2011

Final Paper Due: February 2, 2011

Workshop: February 9, 2011

Committees

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Organizers

Holger Fröning, Universität Heidelberg, Germany

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Pedro Javier García García, Universidad de Castilla-La Mancha, Spain

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